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| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. | |
|---|-----------------|----------------------|---------------------|------------------|--|
| 10/037,490 | 12/28/2001 | Bernd Clauberg | US010726 | 6482 | |
| 24737 7 | 7590 08/25/2003 | | | | |
| PHILIPS INTELLECTUAL PROPERTY & STANDARDS P.O. BOX 3001 | | | EXAM | EXAMINER | |
| | | | DINH, TR | DINH, TRINH VO | |
| BRIARCLIFF | MANOR, NY 10510 | | | | |
| | | | ART UNIT | PAPER NUMBER | |
| | | | 2821 | • | |
| DATE MAILED: 08/25/200 | | | | | |

Please find below and/or attached an Office communication concerning this application or proceeding.

| | Application No. | Applicant(s) | | | | |
|---|--|---|---|--|--|--|
| Advisory Action | 10/037,490 | CLAUBERG ET AL. | | | | |
| Advisory Action | Examiner | Art Unit | | | | |
| | Trinh Vo Dinh | 2821 | | | | |
| The MAILING DATE of this communicati n appe | ars on the cover sheet with the c | orrespondence add | ress | | | |
| THE REPLY FILED 04 August 2003 FAILS TO PLACE T Therefore, further action by the applicant is required to ave final rejection under 37 CFR 1.113 may only be either: (1) condition for allowance; (2) a timely filed Notice of Appeal Examination (RCE) in compliance with 37 CFR 1.114: | oid abandonment of this applica a timely filed amendment which | ition. A proper reply n places the applica | y to a ition in | | | |
| PERIOD FOR RE | PLY [check either a) or b)] | | | | | |
| a) The period for reply expires 2 months from the mailing date b) The period for reply expires on: (1) the mailing date of this A no event, however, will the statutory period for reply expire Is ONLY CHECK THIS BOX WHEN THE FIRST REPLY WAS 706.07(f). Extensions of time may be obtained under 37 CFR 1.136(a). The fee have been filed is the date for purposes of determining the period of fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of (2) as set forth in (b) above, if checked. Any reply received by the Offic timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.17(a) is calculated from: (1) the expiration date of (2) as set forth in (b) above, if checked. Any reply received by the Offic timely filed, may reduce any earned patent term adjustment. | Advisory Action, or (2) the date set forth ater than SIX MONTHS from the mailing FILED WITHIN TWO MONTHS OF THE date on which the petition under 37 CFI f extension and the corresponding amount the shortened statutory period for reply the later than three months after the mail | g date of the final rejecting FINAL REJECTION. R 1.136(a) and the approper the final representation of the fee. The appropriginally set in the final | on. See MPEP opriate extension ropriate extension Office action; or | | | |
| 1. A Notice of Appeal was filed on Appellant's 37 CFR 1.192(a), or any extension thereof (37 CFF | | | | | | |
| 2. The proposed amendment(s) will not be entered be | ecause: | . • | | | | |
| (a) Ithey raise new issues that would require further | er consideration and/or search (s | see NOTE below); | • | | | |
| (b) they raise the issue of new matter (see Note b | elow); | | | | | |
| (c) they are not deemed to place the application in issues for appeal; and/or | n better form for appeal by mate | rially reducing or sir | nplifying the | | | |
| (d) they present additional claims without canceling | ng a corresponding number of fi | nally rejected claim | S. | | | |
| NOTE: See Continuation Sheet. | | | | | | |
| 3. Applicant's reply has overcome the following reject | ion(s): | | | | | |
| 4. Newly proposed or amended claim(s) would canceling the non-allowable claim(s). | be allowable if submitted in a se | parate, timely filed | amendment | | | |
| 5. ☐ The a) ☐ affidavit, b) ☐ exhibit, or c) ☐ request for application in condition for allowance because: | | dered but does NO | T ⁻ place the | | | |
| 6. The affidavit or exhibit will NOT be considered becaraised by the Examiner in the final rejection. | ause it is not directed SOLELY to | o issues which were | e newly | | | |
| 7. For purposes of Appeal, the proposed amendment explanation of how the new or amended claims we | (s) a)⊠ will not be entered or b) ould be rejected is provided belo | ☐ will be entered a w or appended. | and an | | | |
| The status of the claim(s) is (or will be) as follows: | | | | | | |
| Claim(s) allowed: | • | | | | | |
| Claim(s) objected to: <u>14,18-21 and 25-28</u> . | | • | | | | |
| Claim(s) rejected: <u>11-13,15-17,22-24,29-30</u> . | | | | | | |
| Claim(s) withdrawn from consideration: | | | | | | |
| 8. The proposed drawing correction filed on is | a)☐ approved or b)☐ disappi | roved by the Exami | ner. | | | |
| 9. Note the attached Information Disclosure Statemen | | • | | | | |
| 10. Other: | Don Wong | .). | | | | |
| Supervisory Patent Examiner | | | | | | |
| Technology Ceriter 2800 | | | | | | |

U.S. Patent and Trademark Office PTO-303 (Rev. 04-01)

Advisory Action

Part of Paper No. 11

Continuation Sheet (PTO-303)

Continuation of 2. NOTE:

The following are amended limitations, which raise new issues that would require further consideration:

In claim 11 and 15, "said first resonant inductor connected in series to said inverter, and said first resonant capacitor or said first capacitor array connected in series between said first resonant inductor and said first LED array".

In claims 13 and 18, "said first resonant inductor connected in series to said inverter, and said second resonant capacitor or said second capacitor array connected in series between said first resonant inductor and said second LED array".

In claim 14 and 20, "said second resonant inductor connected in series to said inverter, and said second resonant capacitor connected in series between said second resonant inductor and said second LED array".

In claim 22, "said first resonant impedance resonant circuit connected in series between said inverter and said first LED array".

In claim 25, "said first resonating impedance circuit connected in series between said inverter and said second LED array".

In claim 27, "said second resonating impedance circuit connected in series between said inverter and said second LED array".

In claim 29, "said resonating impedance means connected in series between said inverter and each LED array".